DS1100PED

1100 Watts

Distributed Power System

Front-end Bulk Power Total Output Power: 1100W continuous Wide Input Voltage 90 to 264Vac



Special Features

- 1100W output power
- High-power and short form factor
- 1U power supply
- High-density design: 24W/in3
- Active Power Factor Correction
- EN61000-3-2 Harmonic compliance
- Inrush current control
- 80 plus Platinum Efficiency
- N+1 or N+N Redundant
- Hot-pluggable
- Active current sharing
- Full Digital control
- PMBus Compliant
- Accurate input power reporting
- Compatible with Emerson's Universal PMBus GUI
- Reverse airflow option
- Two-year Warranty

Compliance:

- Conducted/Radiated EMI FCC Docket 20780 Part 15 Subpart J Class A Limits + 6dB margin
- EN61000-4-11

Safety

- UL/cUL 60950 (UL Recognized)
- NEMKO+ CB Report EN60950
- EN60950
- CE Mark
- China CCC

Electrical Specifications

Input	
Input range:	90 - 264Vac
Frequency:	47Hz to 63Hz
Efficiency:	94.0% peak
Max Input Current:	14Arms
Inrush Current:	55Apk
Conducted EMI:	Class A
Radiated EMI:	Class A
Power Factor:	>0.9 beginning at 20% load
ITHD:	10%
Leakage Current:	1.4mA
Hold-up Time:	10ms at full load



Electrical Specifications

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OUTPUTS			
Main DC Output	MIN	NOM	MAX
Nominal Setting:	-0.20%	12	0.20%
Total Output Regulation Range	11.4V		12.6V
Dynamic Load Regulation Range:	11.4V		12.6V
Output Ripple:			120mVp-p
Adjustment Range:		TBD	
Output Current:	2A ⁴		91.6A
Current Sharing:		Within +/-5% of full	load rating
Capacitive Loading:	2000uF		40000uF
Start-up from AC to Output:			2200ms
Output Rise Time:	5ms		50ms
Standby DC Output			
Output Setpoint Range:	-1%	12	1%
Total Output Regulation Range:	11.4V		12.6V
Dynamic Load Regulation Range:	11.4V		12.6V
Output Ripple:			120mVp-p
Adjustment Range:		N/A	
Output Current:	0.1A		3.0A
Current Sharing:		N/A	
Capacitive Loading:	100uF		680uF
Start-up from AC to Output:			1700ms
Output Rise Time:	2ms		60ms
PROTECTIONS			
Main Output			
Over-Current Protection ² :	120%		150%
Over-Voltage Protection ¹ :	13.5V		15.0V
Under-Voltage Protection:	10.5V		11.0V
Over-Temperature Protection:		Yes	
Fan Fault Protection:		Yes	
Standby Output			
Over-Current Protection ³ :	120%		150%
Over-Voltage Protection ³ :	13.5V		15.0V
Jnder-Voltage Protection:	10.0V		11.0V
Under-Voltage Protection:	10.0V		11.0V

¹Latch mode ²Autorecovery if the overcurrent is less than 120% and last only for <500ms ³Standby protection is auto-recovery

 $^{^4\}mathrm{SMinimum}$ starting current during transient load. Output stays within regulation range at zero load.

Electrical Specifications

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CONTROL AND STATUS SIG	GNALS		
Input Signals			
PSON			
Active LOW signal which enables/disab Recommended pull-up resistor to 12VS	es the main output. Pulling this signal LOW will turn- B is 8.2k with a 3.0k pull-down to ground. A 100pF d	on the main output. ecoupling capacitor is also	o recommended.
		MIN	MAX
V _{IL}	Input logic level LOW		0.8V
V _{IH}	Input logiv level HIGH	2.0V	5.0V
I _{SOURCE}	Current that may be sourced by this pin		2mA
Isink	Current that may be sunk by this pin at low state		0.5mA
PSKILL			
First break/Last Mate active LOW signal	which enables/disables the main output. This signal	। will have to be pulled to g	round at the system
side with a 2200hm resistor. A 100pF d	ecoupling capacitor is also recommended.	MIN	MAX
V _{II}	Input logic level LOW		0.8V
V _{IH}	Input logiv level HIGH	2.0V	5.0V
1	Current that may be coursed by this pin		2mA
	Current that may be sourced by this pin Current that may be sunk by this pin at low state		0.5mA
Output Signals	Current triat may be sunk by this pin at low state		0.3111/1
is within the operating range while a lo	AC input to the power supply. A logic level HIGH will gic level LOW will indicate that AC has been lost. This pin is pulled high by a 1.0kohm resistor connecte ed to a 100pF decoupling capacitor and pulled down	ed to 3.3V inside the power	
recommended that this pin be connect	ed to a 100pt decoupling capacitor and pulled down	MIN	MAX
VII	Output logic level LOW		0.6V
V _{IH}	Output logic level HIGH	2.0V	5.0V
Isource	Current that may be sourced by this pin		3.3mA
I _{SINK}	Current that may be sunk by this pin at low state		0.7mA
PWR_GOOD/PWOK			
Signal used to indicate that main outpu	tput voltage is within regulation range. The PWR_GOOD signal will be driven HIGH when the output IWW when the output falls below the under-voltage threshold. In the coupling series of the series of		H when the output
This signal also gives an advance warning More details in the Timing Section. This is an open collector/drain output.	ng when there is an impending power loss due to loss Fhis pin is pulled high by a 1.0kohm resistor connecte	of AC input or system shed to 3.3V inside the power	utdown request.
This signal also gives an advance warning More details in the Timing Section. This is an open collector/drain output.	ng when there is an impending power loss due to loss	of AC input or system shed to 3.3V inside the power	utdown request.
This signal also gives an advance warni More details in the Timing Section. This is an open collector/drain output. recommended that this pin be connect	ng when there is an impending power loss due to loss Fhis pin is pulled high by a 1.0kohm resistor connecte	of AC input or system shed to 3.3V inside the power by a 10kohm resistor.	utdown request. er supply. It is
This signal also gives an advance warni More details in the Timing Section. This is an open collector/drain output. recommended that this pin be connect	ng when there is an impending power loss due to loss This pin is pulled high by a 1.0kohm resistor connecte ed to a 100pF decoupling capacitor and pulled down Output logic level LOW	ed to 3.3V inside the power by a 10kohm resistor.	utdown request. er supply. It is MAX 0.8V
This signal also gives an advance warni More details in the Timing Section. This is an open collector/drain output. recommended that this pin be connect V _{II}	ng when there is an impending power loss due to loss This pin is pulled high by a 1.0kohm resistor connecte ed to a 100pF decoupling capacitor and pulled down Output logic level LOW Output logic level HIGH	of AC input or system shed to 3.3V inside the power by a 10kohm resistor.	utdown request. er supply. It is MAX
This signal also gives an advance warning More details in the Timing Section. This is an open collector/drain output. recommended that this pin be connected that this pin be connected. VII VIII	ng when there is an impending power loss due to loss This pin is pulled high by a 1.0kohm resistor connecte ed to a 100pF decoupling capacitor and pulled down Output logic level LOW	ed to 3.3V inside the power by a 10kohm resistor.	utdown request. er supply. It is MAX 0.8V

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Electrical Specifications

CONTROL AND STATUS SIG	GNALS		
Output Signals			
PS_PRESENT			
This pin is shorted to the standby return	at a power supply is inserted in the power bay. n in the power supply. B is 8.2k with a 3.0k pull-down to ground. A 100pF c	lecoupling capacitor is als	so recommended.
PS INTERRUPT			
This event can be triggered by faults su	ply to indicate to the system that a change in power ch as OVP, OCP, OTP, and fan fault. This signal can be tor to 12VSB is 8.2k with a 3.0k pull-down to grounc	e cleared by a CLEAR FAL	JLT
		MIN	MAX
V _{IL}	Output logic level LOW		0.8V
V _{IH}	Output logic level HIGH	2.0V	5.0V
I _{SOURCE}	Current that may be sourced by this pin		4mA
l _{SINK}	Current that may be sunk by this pin at low state		4mA
BUS Signals			
ISHARE			
ISHARE Bus signal used by the power supply for bus voltage inorder to load share.	active current sharing. All power supplies configure	d in the system for n+n sh	naring will refer to this
Bus signal used by the power supply for bus voltage inorder to load share.	active current sharing. All power supplies configured the range of this signal for active sharing will be up to 8.0V, which corresponds to the maximum output current.		
Bus signal used by the power supply for bus voltage inorder to load share. VOLTAGE RANGE	The range of this signal for active sharing will be up to 8.0V, which corresponds to the maximum output current.	MIN	MAX
Bus signal used by the power supply for bus voltage inorder to load share. VOLTAGE RANGE	The range of this signal for active sharing will be up to 8.0V, which corresponds to the maximum output current. Voltage at 100% load, stand-alone unit	MIN 7.75	MAX 8.25V
Bus signal used by the power supply for bus voltage inorder to load share. VOLTAGE RANGE	The range of this signal for active sharing will be up to 8.0V, which corresponds to the maximum output current. Voltage at 100% load, stand-alone unit Voltage at 50% load, stand-alone unit	MIN 7.75 3.85V	MAX 8.25V 4.15V
Bus signal used by the power supply for bus voltage inorder to load share. VOLTAGE RANGE I _{SHARE} Voltage	The range of this signal for active sharing will be up to 8.0V, which corresponds to the maximum output current. Voltage at 100% load, stand-alone unit Voltage at 50% load, stand-alone unit Voltage at 0% load, stand-alone unit	MIN 7.75	MAX 8.25V 4.15V 0.3
Bus signal used by the power supply for bus voltage inorder to load share. VOLTAGE RANGE ISHARE Voltage	The range of this signal for active sharing will be up to 8.0V, which corresponds to the maximum output current. Voltage at 100% load, stand-alone unit Voltage at 50% load, stand-alone unit	MIN 7.75 3.85V	MAX 8.25V 4.15V
Bus signal used by the power supply for bus voltage inorder to load share. VOLTAGE RANGE ISHARE Voltage	The range of this signal for active sharing will be up to 8.0V, which corresponds to the maximum output current. Voltage at 100% load, stand-alone unit Voltage at 50% load, stand-alone unit Voltage at 0% load, stand-alone unit Current that may be sourced by this pin	MIN 7.75 3.85V 0	MAX 8.25V 4.15V 0.3 160mA
Bus signal used by the power supply for bus voltage inorder to load share. VOLTAGE RANGE ISHARE Voltage	The range of this signal for active sharing will be up to 8.0V, which corresponds to the maximum output current. Voltage at 100% load, stand-alone unit Voltage at 50% load, stand-alone unit Voltage at 0% load, stand-alone unit Current that may be sourced by this pin	MIN 7.75 3.85V 0	MAX 8.25V 4.15V 0.3 160mA
Bus signal used by the power supply for bus voltage inorder to load share. VOLTAGE RANGE ISHARE Voltage ISOURCE SCL,SDA Clock and data signals defined as per I2 resistor to 3.3V and a 100pF decouplin	The range of this signal for active sharing will be up to 8.0V, which corresponds to the maximum output current. Voltage at 100% load, stand-alone unit Voltage at 50% load, stand-alone unit Voltage at 0% load, stand-alone unit Current that may be sourced by this pin	MIN 7.75 3.85V 0	MAX 8.25V 4.15V 0.3 160mA
Bus signal used by the power supply for bus voltage inorder to load share. VOLTAGE RANGE ISHARE Voltage ISOURCE SCL,SDA Clock and data signals defined as per 12 resistor to 3.3V and a 100pF decouplin VI	The range of this signal for active sharing will be up to 8.0V, which corresponds to the maximum output current. Voltage at 100% load, stand-alone unit Voltage at 50% load, stand-alone unit Voltage at 0% load, stand-alone unit Current that may be sourced by this pin	MIN 7.75 3.85V 0	MAX 8.25V 4.15V 0.3 160mA

Note: All signal noise levels are below 400mVpk-pk from 0-100MHz. 12C Addressing Table: Not applicable. This power supply has a fixed 12C address. In order to support multiple addresses, the system will have to utilize a switcher or an 12C expander.

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Electrical Specifications

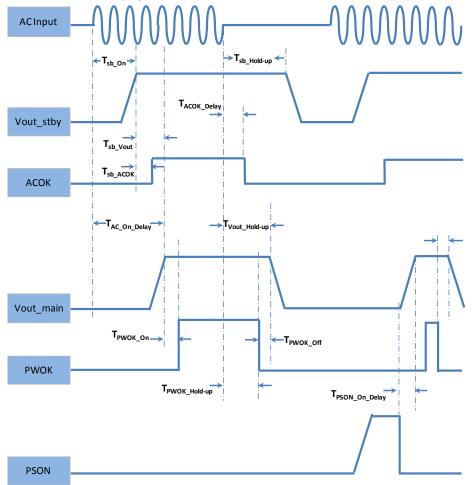
LED INDICATORS A single bi-color LED is used to indicate the po	ower supply st	tatus.	
	Status LED		
NO AC INPUT TO PSU	Off		
AC PRESENT, STBY ON, MAIN OUTPUT OFF			
MAIN OUTPUT ON	Solid GREEN		
POWER SUPPLY FAILURE (OCP, OVP, OTP, Blinking AMBER FAN FAULT)			
FIRMWARE REPORTING AND MO	NITORING	ł	
	ACC	CURACY RANGE	
OUTPUT LOADING	5 to 20%	20% to 50%	50% to 100%
INPUT VOLTAGE	+/-5%		
INPUT CURRENT	+/-0.55A fixe	ed error	+/-4%
INPUT POWER	+/-1.25W at	<125W input	+/-1.25%
OUTPUT VOLTAGE	+/-2%		
OUTPUT CURRENT	0.3A fixed e	тог	+/-2%
TEMPERATURE	+/-5degC on	the operating rang	e
E _{IN}	+/-15% from 10% to 20% load +/-5%		+/-5%
PMBUS			
REMOTE ON/OFF	YES		

Electrical Specifications

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	DESCRIPTION	MIN	MAX	UNIT
T _{sb_On}	Delay from AC being applied to standby output being within regulation	20	1700	ms
^T sb_ACOK	Delay from standby output to ACOK assertion	See note below	20	ms
「sb_Vout	Delay from standby output to main output voltage being within regulation		300	ms
TAC_On_Delay	Delay from AC being applied to main output being within regulation		2200	ms
T _{PWR_GOOD_On}	Delay from output voltages within regulation limits to PWOK asserted	100	1000	ms
「ACOK_Delay	Delay from loss of AC to assertion of ACOK		6	ms
PWR_GOOD_Hold-up	Delay from loss of AC to deassertion of PWOK	10		ms
「Vout_Hold-up	Delay from loss of AC to main output being within regulation	11		ms
rsb_Hold-up	Delay from loss of AC to standby output being within regulation	150		ms
PWR_GOOD_Off	Delay from deassertion of PWOK to output falling out of regulation	1		ms
PSON_On_Delay	Delay from PSON assertion to output being within regulation		350	ms
TPWOK_Low	Duration of PWOK being in deasserted state during an ON/OFF cycle of PSU	N/A	N/A	

Notes: Tvout_hold-up tested at 1A load on standby output Tsb_ACOK: ACOK can assert earlier than the standby output



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Environmental Specifications

Operating Temperatue:	10 to 50°C
Operating Altitude:	up to 10,000 feet
Operating Relative Humidity:	20% to 80% non-condensing
Non-operating Temperature:	-40 to 70°C
Non-operating Relative Humidity:	10% to 95% non-condensing
Non-operating Altitude:	up to 50,000 feet

Vibration and Shock:	Astec Standard QP3205, IEC721-3-3 3M3 and IEC721-3-3 1M3
ROHS Compliance:	Yes

MTBF	400,000 hours using Bell Core TR-332, issue 6 specification, Method 1 Case 3 at 25degC ambient at full load.
Operating Life	Minimum of 5 years
Reliability	All electronic component derating analysis and capacitor life calculation is done at maximum ambient, 80% of maximum rated load, nominal input line voltage.

Output Connector

Output Connector Part Number Card-edge

Mating Connector Part Number FCI 10107844-002LF

Power Supply Output Card Edge (Bottom Side)

524 513

P29-P36 P21-28

P21-28		
0	P19/20	

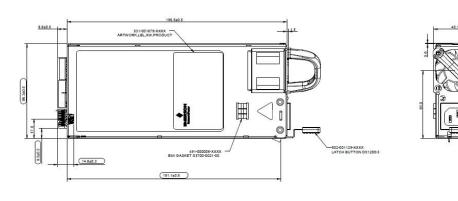
	~	
S1	PS PRESENT	
S2	Reserved	
S3	Reserved	
S4	Pwr_Good	
S5	ACOK (AC Input Present)	
S6	RTN	
S7	I-MON	
S8	RESERVE	
S9	PS INTERRUPT	
S10	RTN	
S11	Reserved	
S12	Reserved	
P1-P8	Vo	
P9-P18	RTN	

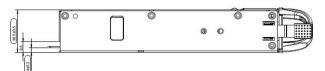
Power Supply Output Card Edge (Top Side)

S13	PS_ON
S14	PS_KILL
S15	Reserved
S16	RTN
S17	SDA
S18	R
S19	SCL
S20	RTN
S21	REMOTE SENSE-
S22	RTN
S23	REMOTE SENSE+
S24	RESERVE
P19-P20	VSB
P21-P28	RTN
P29-P36	Vo

Mechanical Outline







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